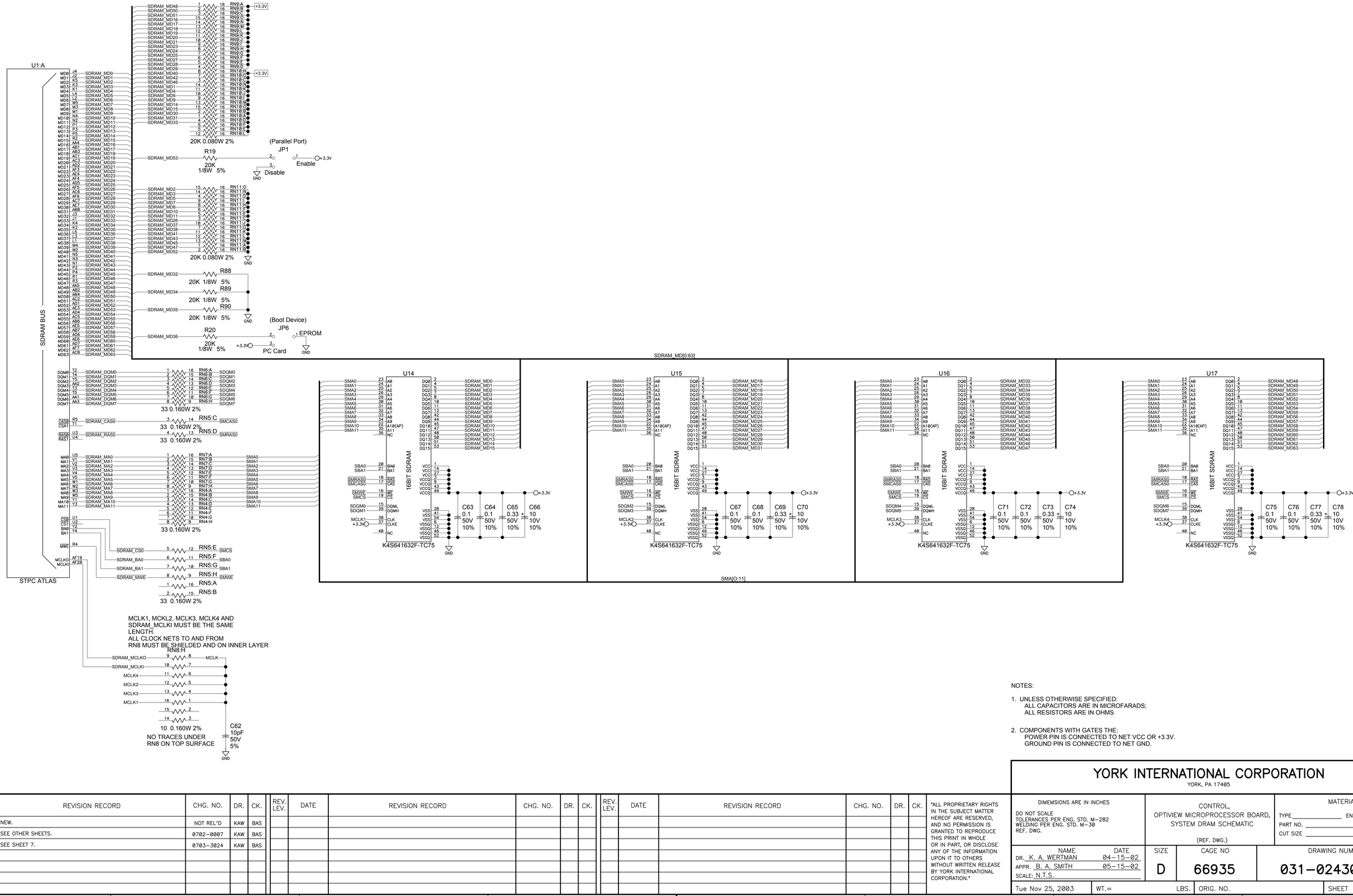

NOTES:

1. UNLESS OTHERWISE SPECIFIED:
   ALL CAPACITORS ARE IN MICROFARADS;
   ALL RESISTORS ARE IN OHMS

2. COMPONENTS WITH GATES THE:
   POWER PIN IS CONNECTED TO NET VCC OR +3.3V.
   GROUND PIN IS CONNECTED TO NET GND.

REV. LEV.	DATE	REVISION RECORD	CHG. NO.	DR.	CK.
–		NEW.	NOT REL'D	KAW	BAS
A	04–03–03	SEE OTHER SHEETS.	0702–0007	KAW	BAS
B	10–17–03	SEE SHEET 7.	0703–3024	KAW	BAS

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YORK INTERNATIONAL CORPORATION

YORK, PA 17405

DIMENSIONS ARE IN INCHES
DO NOT SCALE
TOLERANCES PER ENG. STD. M–282
WELDING PER ENG. STD. M–30
REF. DWG.

CONTROL,
OPTIVIEW MICROPROCESSOR BOARD,
SYSTEM DRAM SCHEMATIC
(REF. DWG.)

MATERIAL
TYPE _____ ENG. STD. E–355
PART NO. _____
CUT SIZE _____

NAME	DATE
DR. K. A. WERTMAN	04–15–02
APPR. B. A. SMITH	05–15–02
SCALE: N.T.S.	

SIZE: D
CAGE NO: 66935
DRAWING NUMBER: 031–02430–999

Tue Nov 25, 2003 | WT.= LBS. | ORIG. NO. | SHEET 3 OF 9

031–02430–999_b0.sch